



COPY OF PAPERS
ORIGINALLY FILED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Craig T. Salling, et al.

Art Unit: TBD

Serial No.: 10/051,639

Examiner: TBD

Filed: 01/18/02

Docket: TI-32536

4 / 4 formal
drawings
C. Willis
5-21-02

For: ESD Improvement by a Vertical Bipolar Transistor with Low Breakdown Voltage
and High Beta

LETTER TO THE OFFICIAL DRAFTSPERSON

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited
with the U.S. Postal Service on 3-27-02
as First Class Mail in an envelope addressed to: Assistant
Commissioner for Patents, Washington, D.C. 20231.

Karen Vertz
Karen Vertz

3-27-02
Date

Assistant Commissioner for
Patents
Washington, D. C. 20231

Dear Sir:

Enclosed are **TWO (2)** sheets of formal drawings for the above-referenced case.
Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments
Incorporated. This sheet is enclosed in triplicate.

Texas Instruments Incorporated
P.O. Box 655474 M/S 3999
Dallas, Texas 75265
(214) 939-8651

Respectfully submitted,

Gary C. Honeycutt
Gary C. Honeycutt
Reg. No. 20,250
Attorney for Applicants



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
10/051,639	01/18/2002	Craig T. Salling	TI-32536

CONFIRMATION NO. 8729

FORMALITIES LETTER



OC00000007660146

COPY OF PAPERS
ORIGINALLY FILED

Date Mailed: 03/18/2002

NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION

FILED UNDER 37 CFR 1.53(b)

Filing Date Granted

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given **TWO MONTHS** from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The oath or declaration is unsigned.
- To avoid abandonment, a late filing fee or oath or declaration surcharge as set forth in 37 CFR 1.16(l) of \$130 for a non-small entity, must be submitted with the missing items identified in this letter.
- The balance due by applicant is \$ 130.

The application is informal since it does not comply with the regulations for the reason(s) indicated below.

The required item(s) identified below must be timely submitted to avoid abandonment:

- Substitute drawings in compliance with 37 CFR 1.84 because:
 - drawing sheets do not have the appropriate margin(s) (see 37 CFR 1.84(g)). Each sheet must include a top margin of at least 2.5 cm. (1 inch), a left side margin of at least 2.5 cm. (1 inch), a right side margin of at least 1.5 cm. (5/8 inch), and a bottom margin of at least 1.0 cm. (3/8 inch);

*A copy of this notice **MUST** be returned with the reply.*

Trehan H

Customer Service Center
Initial Patent Examination Division (703) 308-1202

09/2002 JAD001 00000003 200668
FC:105 130.00 CH 10051639

[illegible]



COPY OF PAPERS
ORIGINALLY FILED

MP

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Craig T. Salling, et al.

Docket No.: TI-32536

Art Unit: 2811

Serial No: 10/051,639

Filed: 01/18/02

For: ESD Improvement by a Vertical Bipolar Transistor with Low Breakdown Voltage and High Beta

Assistant Commissioner for
Patents
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being deposited
with the U.S. Postal Service on
3-27-02 as First Class Mail in an
envelope addressed to: Assistant Commissioner for Patents,
Washington, D.C.

Karen Vertz 3-27-02
Karen Vertz Date

RESPONSE TO NOTICE TO FILE MISSING PARTS OF APPLICATION

Dear Sir:

In response to the Notice of Missing Parts dated March 18, 2002 please charge the fee of \$130.00 for the surcharge under 37 CFR 1.27 to Deposit Account No. 20-0668 of Texas Instruments Incorporated.

Please find enclosed the Declaration/Power of Attorney (1 page), an Assignment with Form PTO 1595 and formal drawings for the above-referenced case. The Commissioner is hereby authorized to charge any additional fees which may be required. **This sheet is submitted in triplicate.**

Respectfully submitted,

Gary C. Honeycutt
Gary C. Honeycutt
Attorney for Applicants
Reg. No. 20,250

Texas Instruments Incorporated
Mail Station 3999
P.O. Box 655474
Dallas, TX 75265
(214) 939-8651



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Craig T. Salling, et al.

Art Unit: TBD

Serial No.: 10/051,639

Examiner: TBD

Filed: 01/18/02

Docket: TI-32536

For: ESD Improvement by a Vertical Bipolar Transistor with Low Breakdown Voltage and High Beta

LETTER TO THE OFFICIAL DRAFTSPERSON

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service on 3-27-02 as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.

Karen Vertz
Karen Vertz

3-27-02
Date

Assistant Commissioner for
Patents
Washington, D. C. 20231

Dear Sir:

Enclosed are **TWO (2)** sheets of formal drawings for the above-referenced case. Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments Incorporated. This sheet is enclosed in triplicate.

Texas Instruments Incorporated
P.O. Box 655474 M/S 3999
Dallas, Texas 75265
(214) 939-8651

Respectfully submitted,

Gary C. Honeycutt
Gary C. Honeycutt
Reg. No. 20,250
Attorney for Applicants